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\*/

#ifndef \_INTCTRL\_H

#define \_INTCTRL\_H

/\*\*

\* \file rp2040/intctrl.h

\*/

#ifdef \_\_ASSEMBLER\_\_

#define TIMER\_IRQ\_0 0

#define TIMER\_IRQ\_1 1

#define TIMER\_IRQ\_2 2

#define TIMER\_IRQ\_3 3

#define PWM\_IRQ\_WRAP 4

#define USBCTRL\_IRQ 5

#define XIP\_IRQ 6

#define PIO0\_IRQ\_0 7

#define PIO0\_IRQ\_1 8

#define PIO1\_IRQ\_0 9

#define PIO1\_IRQ\_1 10

#define DMA\_IRQ\_0 11

#define DMA\_IRQ\_1 12

#define IO\_IRQ\_BANK0 13

#define IO\_IRQ\_QSPI 14

#define SIO\_IRQ\_PROC0 15

#define SIO\_IRQ\_PROC1 16

#define CLOCKS\_IRQ 17

#define SPI0\_IRQ 18

#define SPI1\_IRQ 19

#define UART0\_IRQ 20

#define UART1\_IRQ 21

#define ADC\_IRQ\_FIFO 22

#define I2C0\_IRQ 23

#define I2C1\_IRQ 24

#define RTC\_IRQ 25

#else

/\*\*

\* \brief Interrupt numbers on RP2040 (used as typedef \ref irq\_num\_t)

\* \ingroup hardware\_irq

\*/

typedef enum irq\_num\_rp2040 {

TIMER\_IRQ\_0 = 0, ///< Select TIMER's IRQ 0 output

TIMER\_IRQ\_1 = 1, ///< Select TIMER's IRQ 1 output

TIMER\_IRQ\_2 = 2, ///< Select TIMER's IRQ 2 output

TIMER\_IRQ\_3 = 3, ///< Select TIMER's IRQ 3 output

PWM\_IRQ\_WRAP = 4, ///< Select PWM's IRQ\_WRAP output

USBCTRL\_IRQ = 5, ///< Select USBCTRL's IRQ output

XIP\_IRQ = 6, ///< Select XIP's IRQ output

PIO0\_IRQ\_0 = 7, ///< Select PIO0's IRQ 0 output

PIO0\_IRQ\_1 = 8, ///< Select PIO0's IRQ 1 output

PIO1\_IRQ\_0 = 9, ///< Select PIO1's IRQ 0 output

PIO1\_IRQ\_1 = 10, ///< Select PIO1's IRQ 1 output

DMA\_IRQ\_0 = 11, ///< Select DMA's IRQ 0 output

DMA\_IRQ\_1 = 12, ///< Select DMA's IRQ 1 output

IO\_IRQ\_BANK0 = 13, ///< Select IO\_BANK0's IRQ output

IO\_IRQ\_QSPI = 14, ///< Select IO\_QSPI's IRQ output

SIO\_IRQ\_PROC0 = 15, ///< Select SIO\_PROC0's IRQ output

SIO\_IRQ\_PROC1 = 16, ///< Select SIO\_PROC1's IRQ output

CLOCKS\_IRQ = 17, ///< Select CLOCKS's IRQ output

SPI0\_IRQ = 18, ///< Select SPI0's IRQ output

SPI1\_IRQ = 19, ///< Select SPI1's IRQ output

UART0\_IRQ = 20, ///< Select UART0's IRQ output

UART1\_IRQ = 21, ///< Select UART1's IRQ output

ADC\_IRQ\_FIFO = 22, ///< Select ADC's IRQ\_FIFO output

I2C0\_IRQ = 23, ///< Select I2C0's IRQ output

I2C1\_IRQ = 24, ///< Select I2C1's IRQ output

RTC\_IRQ = 25, ///< Select RTC's IRQ output

IRQ\_COUNT

} irq\_num\_t;

#endif

#define isr\_timer\_0 isr\_irq0

#define isr\_timer\_1 isr\_irq1

#define isr\_timer\_2 isr\_irq2

#define isr\_timer\_3 isr\_irq3

#define isr\_pwm\_wrap isr\_irq4

#define isr\_usbctrl isr\_irq5

#define isr\_xip isr\_irq6

#define isr\_pio0\_0 isr\_irq7

#define isr\_pio0\_1 isr\_irq8

#define isr\_pio1\_0 isr\_irq9

#define isr\_pio1\_1 isr\_irq10

#define isr\_dma\_0 isr\_irq11

#define isr\_dma\_1 isr\_irq12

#define isr\_io\_bank0 isr\_irq13

#define isr\_io\_qspi isr\_irq14

#define isr\_sio\_proc0 isr\_irq15

#define isr\_sio\_proc1 isr\_irq16

#define isr\_clocks isr\_irq17

#define isr\_spi0 isr\_irq18

#define isr\_spi1 isr\_irq19

#define isr\_uart0 isr\_irq20

#define isr\_uart1 isr\_irq21

#define isr\_adc\_fifo isr\_irq22

#define isr\_i2c0 isr\_irq23

#define isr\_i2c1 isr\_irq24

#define isr\_rtc isr\_irq25

#endif // \_INTCTRL\_H